

**Department
of
Electrical and Computer Engineering, UC.
EE - 352
ELECTRONICS II
SPRING QUARTER 2010**

**HOMEWORK ASSIGNMENT #7
Due June 2, 2010**

Final Exam: 20 EECE-352 001 6/9 Wednesday 2:15-4:15pm Swift 519

Homework:

Problem #1: Design a tuned amplifier.

- a) Pick one of the available transistors (BJT or FET) in Pspice files
- b) Pick a center frequency ω_0
- c) Bias it properly by choosing the right resistors.
- d) Simulate your circuit on Pspice and verify that your simulated result comes close to your design frequency.
- e) Submit your complete Circuit, Schematic in PSpice and the Probe results.